

Amin Taghizadeh

FPGA and Embedded System Engineer



درباره من

From simple Image Processing and Neural Networks to advanced low level HDL programming, From basic consumer requested 3D model design to complicated Technology Mapping, From Concept to MVP and Final Product, I've participated in much technological projects in different levels of various subjects. With a degree in Digital Engineering from well-known university, my recent works have been narrowed -but not limited- to FPGA and Digital Signal Processing Domain. Nevertheless, the journey toward a Full Stack high performance- High reliable analyzer, designer and innovator continues, and I'll take every chance to extend my expertise. in order "To Make Ideas Come True".

Skilled at Organizing, Documentation and working with high amount of Data
Highly detail oriented attitude , Troubleshooting Skill
Prefer to see Ideas, Dreams and "what could be"
Philosophical preparation, plan ahead manner

تجربه‌های کاری

FPGA and Embedded Hardware Engineer

مرداد ۱۳۹۸ تا حالا | Sepehran Development of Wave Technology

FPGA Implementation of DSP and Communication Algorithms (Xilinx 7 Series, E.g., V7690)

Firmware and DSP (TI Multicore) Embedded Programming, RTL Coding, Model analysis

Plan, Simulation, Implement, Integration, Verification and Debug Signal Processing Algorithms on FPGA

FPGA and DSP Peripheral Configuration and Setup (Highspeed SPIO, LAN, EMIF and etc.)

Hardware Test and Troubleshooting, Circuit Verification and Performance Analysis

FPGA Based Implementation Researcher and Programmer

فروردین ۱۳۹۶ تا فروردین ۱۳۹۷ | Babol Noshirvani University of Technology

Research Student for the project "Add-on Encryption device for portable storage devices".

Implemented a USB PHY on FPGA Platform

Implemented Rd/Rw of Basic SCSI Protocol Wrapper

BSS, NOC FO Intern

مرداد ۱۳۹۵ تا آبان ۱۳۹۵ | Huawei Technologies Co., Ltd

On Job Training/Internship

Main Roles:

Monitoring BSS NEs Status and Perform Reports

Performing Fault Management Processes

اطلاعات تماس

تماس از طریق جابینجا

اطلاعات شخصی

سال تولد: ۱۳۷۲

وضعیت سربازی: پایان خدمت

وضعیت تأهل: مجرد

زبان‌ها

انگلیسی (حرفه‌ای)

آلمانی (مبتدی)

R&D Engineer

۱۳۹۴ تا فروردین ۱۳۹۳ | Terez Electronics

: Main Roles

Technical Solution architect

Design & Implement new methods

Performing component measurements and characteristics

Consumer 3D model design



اطلاعات تماس

تماس از طریق جابینجا

سوابق تحصیلی

Digital Electronic Systems Engineering (کارشناسی ارشد)

۱۳۹۷ | Tehran Shahid Beheshti University (در حال تحصیل)

Temporarily Paused

Thesis Area (Temporarily Paused): Hardware design and optimization of Neural Networks using FPGA.

The goal of research is to Maximize the reliability of a neural network through determined latency and to reduce total power consumption for portable and high-power usages, as well as making a framework for fast deployment of CNN on FPGA platforms

Electronics Engineering (کارشناسی)

۱۳۹۱ تا ۱۳۹۶ | Babol Nooshirvani University of Technology

Project Area (in the very beginning of the process of the patent registry):

Developing an Encryptor for Some USB Based Devices with FPGA

مهارت‌های حرفه‌ای

. FPGA/VHDL . Logical Thinking/Troubleshooting . Ideation
. Multimedia Edit and Production . PCB Design . Programming Logic
Programming . Data Analysis . Robotics

اطلاعات شخصی

سال تولد: ۱۳۷۲

وضعیت سربازی: پایان خدمت

وضعیت تأهل: مجرد

زبان‌ها

انگلیسی (حرفه‌ای)

آلمانی (مبتدی)